L Number	Hits	Search Text	DB	Time stamp
1	693244		USPAT;	2003/10/09 15:56
-			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
2	11	specify\$3 same interconnect\$3 same component	USPAT;	2003/10/09 16:01
		same optimiz\$5	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_ !	15 40 1540	IBM_TDB	2003/10/09 16:02
3	6	l • • • • • • • • • • • • • • • • • • •	USPAT; US-PGPUB;	2003/10/09 10:02
		same component same optimiz\$5)	EPO; JPO;	
			DERWENT;	
			IBM TDB	
	5235	(optimiz\$3 adj4 integrate adj2 circuit) or	USPAT;	2003/06/03 07:28
	3233	(optimiz\$3 adj3 design\$3)	US-PGPUB;	
		(opermizes adjs designes)	EPO; JPO;	
			DERWENT;	
			ÍBM TDB	
_	l 0	self-programmable adj3 integrated adj2	USPAT;	2003/06/02 14:54
		circuit	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	394		USPAT;	2003/06/02 14:57
		((optimiz\$3 adj4 integrate adj2 circuit) or	US-PGPUB;	
	ĺ	(optimiz\$3 adj3 design\$3))	EPO; JPO;	
	ļ		DERWENT; IBM TDB	
'	0	(self?programmable same integrated adj2	USPAT;	2003/06/02 14:57
-		circuit) and ((soft adj macro) and ((hard	US-PGPUB;	2003/00/02 11.3/
		adj macro) or (firm adj macro)))	EPO; JPO;	
		day macro, or (rirm ad) macro,,,	DERWENT;	
			IBM TDB	
_	,0	((design\$3 adj4 integrated adj circuit) and	USPAT;	2003/06/02 14:58
	,	((optimiz\$3 adj4 integrate adj2 circuit) or	US-PGPUB;	
		(optimiz\$3 adj3 design\$3))) and ((soft adj	EPO; JPO;	
		macro) and ((hard adj macro) or (firm adj	DERWENT;	· ·
		macro)))	IBM_TDB	
-	6	(design\$3 adj4 integrated adj circuit) and	USPAT;	2003/06/02 14:58
		((soft adj macro) and ((hard adj macro) or	US-PGPUB;	
·		(firm adj macro)))	EPO; JPO; DERWENT;	,
			IBM TDB	
	7	((optimiz\$3 adj4 integrate adj2 circuit) or	USPAT;	2003/06/02 15:04
-	′	(optimiz\$3 adj4 integrate adj2 cricuit, or (optimiz\$3 adj3 design\$3)) and ((soft adj	US-PGPUB;	
,		macro) and ((hard adj macro) or (firm adj	EPO; JPO;	-
		macro)))	DERWENT;	
			IBM_TDB	
-	8	self?programmable same integrated adj2	USPAT;	2003/06/02 15:37
		circuit	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		a	IBM_TDB	2003/06/02 15:16
-	12467	design\$3 adj4 integrated adj circuit	USPAT; US-PGPUB;	2003/00/02 13:16
1			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	38	(soft adj macro) and ((hard adj macro) or	USPĀT;	2003/06/02 15:33
		(firm adj macro))	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	2003/06/02 15:24
-	19		USPAT;	2003/06/02 15:34
		adj macro) or (firm adj macro)))	US-PGPUB; EPO; JPO;	
		Į.	DERWENT;	
			IBM TDB	
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			· · · · · · · · · · · · · · · · · · ·		
	-	0	((design\$3 adj4 integrated adj circuit) and ((optimiz\$3 adj4 integrate adj2 circuit) or	USPAT; US-PGPUB;	2003/06/02 15:38
			<pre>(optimiz\$3 adj3 design\$3))) and self?programmable</pre>	EPO; JPO; DERWENT; IBM TDB	•
		2	((optimiz\$3 adj4 integrate adj2 circuit) or (optimiz\$3 adj3 design\$3)) and	USPAT; US-PGPUB;	2003/06/02 15:38
	-		self?programmable	EPO; JPO; DERWENT;	,
		2	(design\$3 adj4 integrated adj circuit) and	IBM_TDB USPAT;	2003/06/02 15:38
			self?programmable	US-PGPUB; EPO; JPO; DERWENT;	
	_	68	self?programmable	IBM_TDB USPAT;	2003/06/02 15:39
			5511, 926, 926, 111, 111, 111, 111, 111, 111, 111, 1	US-PGPUB; EPO; JPO;	
				DERWENT; IBM_TDB	0000/05/00 05 45
		2447	design adj3 environment	USPAT; US-PGPUB;	2003/06/03 07:45
				EPO; JPO; DERWENT; IBM TDB	
	-	274323	optimiz\$3	USPAT; US-PGPUB;	2003/06/03 07:30
	-			EPO; JPO; DERWENT;	
	-	660	(design adj3 environment) and optimiz\$3	IBM_TDB USPAT;	2003/06/03 07:30
			•	US-PGPUB; EPO; JPO;	
		177	macro and ((design adj3 environment) and	DERWENT; IBM_TDB USPAT;	2003/06/03 07:30
ļ	- ,	172	optimiz\$3)	US-PGPUB; EPO; JPO;	2003/03/03/03/07/130
			• .	DERWENT; IBM_TDB	
		111	hard and (macro and ((design adj3 environment) and optimiz\$3))	USPAT; US-PGPUB;	2003/06/03 07:30
	·	• ·		EPO; JPO; DERWENT;	
	-	47	soft and (hard and (macro and ((design adj3 environment) and optimiz\$3)))	IBM_TDB USPAT; US-PGPUB;	2003/06/03 07:31
		,	environment, and opermizes,	EPO; JPO; DERWENT;	
	-	1080	design adj environment	IBM_TDB USPAT;	2003/06/03 07:49
			,	US-PGPUB; EPO; JPO;	
				DERWENT; IBM_TDB	2002/06/02 07 47
	-	54	(soft and macro) and ((hard adj macro) or (firm adj macro))	USPAT; US-PGPUB; EPO; JPO;	2003/06/03 07:47
			,	DERWENT; IBM TDB	
	-	2	(design adj environment) and ((soft and macro) and ((hard adj macro) or (firm adj	USPAT; US-PGPUB;	2003/06/03 07:47
			macro)))	EPO; JPO; DERWENT;	
	-	28	(design adj environment) same optimiz\$3	IBM_TDB USPAT; US-PGPUB;	2003/06/03 07:49
	,			EPO; JPO; DERWENT;	
				IBM_TDB	<u> </u>